

IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.  
SERIAL NO.: 09/748,098  
FILING DATE: December 21, 2000  
TITLE: System and Method for Instruction Level Multithreading in an  
Embedded Processor Using Zero-Time Context Switching  
EXAMINER: David J. Huisman  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 20880-05093

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COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

NOTICE OF APPEAL

SIR:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Primary Examiner, mailed May 23, 2007, finally rejecting claims 1-55.

The Commissioner is hereby authorized to charge to Deposit Account No. 19-2555 the sum of \$250.00 pursuant to 37 CFR § 1.17(e), and any additional charges or fees as applicable in connection with the filing of this Notice of Appeal.

Respectfully submitted,  
NICHOLAS J. KELSEY, ET AL.

Dated: August 23, 2007

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